Foft Collins, Colorado 80527-2400

ATTORNEY DOCKET NO. 10001915-3

NOV 1 7 2003

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s):

P. O. Box 272400

Buswell et al.

Confirmation No.: 9361

Application No.: 10/080,747

Examiner: Do. A.

Filing Date:

02/22/2002

Group Art Unit: 2853

Title:

A Slotted Semiconductor Substrate Having Microelectronics Integrated Thereon

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir:

Transmitted herewith is/are the following in the above-identified application:

Petition to extend time to

Response/Amendment (X) (X) New fee as calculated below

Supplemental Declaration

(Address envelope to "Mail Stop Non-Fee Amendment") No additional fee ( )

Other: Information Disclosure Statement; PTO Form 1449 (X)

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C	\$	[ ] FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM + \$290									
C	\$		1 MON 480.0			3RD MON \$950.00	MONTH 0.00		1ST MONTH \$110.00	EXTENSION FEE	
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86	\$	MENT	TOTAL ADDITIONAL FEE FOR THIS AMENDMENT								

to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees. A duplicate copy of this sheet is enclosed.

"Express	Mail"	label	no.	<u>EV31</u>	693	57	241	<u>JS</u>
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Date of Deposit 11/17/2003

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner Patents, Alexandria, 22313-1450.

Typed Name: Carlin Taylor

Respectfully submitted,

Ву

Paul W. Mitchell

Buswell et al.

Attorney/Agent for Applicant(s)

Reg. No. 44,453

Date: 11/17/2003



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Inventors:** Buswell et al. Examiner:

Do, A.

5 Serial No: 10/080,747

**Group Art Unit:** 

2853

Filing Date: 2/22/02

9361

Title:

**Confirmation No:** 

A slotted Semiconductor Substrate Having Microelectronics Integrated Thereon

To:

Commissioner of Patents and Trademarks

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PO Box 1450

Alexandria, VA 22313-1450

From:

**Dmitry Milikovsky** 

Hewlett-Packard Development Company, L.P.

15

Intellectual Property Administration

P.O. Box 272400

Fort Collins, Colorado 80527-2400

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## **INTRODUCTORY COMMENTS**

This communication is in response to the Office Action dated 8/15/03, for which a three-month shortened statutory period for response is set for 11/15/03. Please amend the above-identified application in accordance the directions set forth below.

25 Amendment to the Claims are reflected on the list of claims which

begins on page 2 of this paper.

Remarks/Arguments begin on page 12 of this paper.

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